



Call for Papers

The 27th IEEE International Symposium on High-Performance Computer Architecture (HPCA) will be held in Seoul, 2/27-3/3 2021. HPCA is a high impact venue for presenting research results on a wide range of computer architectures.

Website: hpcacnf.org.

*** IMPORTANT DATES ***

- **Abstract submission: July 24, 2020**
- **Full paper submission: July 31, 2020**
- **Rebuttal/revision: Sept 28th-Oct 11th, 2020**
- **Notification: October 30th, 2020**

Some topics of interest are listed, but we encourage authors to contact PC chair if they have a question regarding the topic fit:

- Accelerator system design
- Cloud, datacenter, cluster/distributed systems and applications
- FPGAs, CGRAs, and reconfigurable systems
- GPUs
- ILP enhancing microarchitecture techniques
- Memory hierarchy
- Networks and interconnects
- Power efficient architectures
- Quantum and superconducting architectures
- Reliable architectures
- Near data computing
- Machine learning systems, including algorithm/system co-design
- Performance characterization/modeling
- Secure and private computing systems
- Systems designed around emerging/partially proven device technologies
- Ultra-low power computing and associated echo systems

ORGANIZERS

- Program Chair: Prof. Murali Annavaram (USC)
- General Chairs: Prof. Jung Ho Ahn (SNU) and Prof. John Kim (KAIST)